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Software Testing Conducted at Live-Sky Event Hosted by U.S. DHS

BlueSky GNSS Firewall Software Release 2.0 includes charting and advanced threshold settings of Global Navigation Satellite System (GNSS) observables. These improvements are a result of participation in an industry live-sky testing event hosted by the U.S. Department of Homeland Security (DHS) which included GPS threat scenarios. [Review Press Release](#) and [Product Information](#)

Upgrade today to the latest TimePictra Release 10.10

TimePictra release 10.10 is now released and available for download from our support site. Release 10.10 provides key security enhancements as well as full support for TimeProvider 4100 release 2.1. For more information, please log into our support site and search on “TimePictra” for a complete list of available files and SW downloads.

[Support Site](#)

Chip Scale [Atomic Clock \(CSAC\)](#) Now Available in Extended Temperature Range

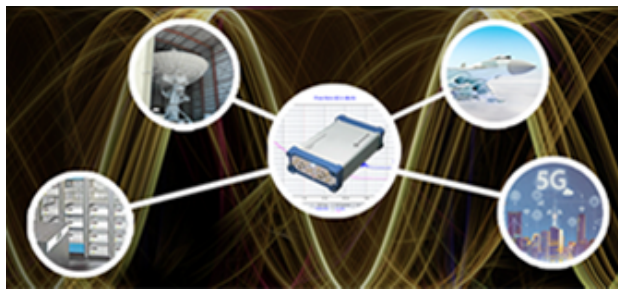
For applications that require a cold start down to -40°C and maximum operating temperature of up to 80°C Microchip offers a version of [CSAC](#) called CSAC Extended Temperature. CSAC Extended Temperature is a standard 10MHz CSAC that has been screened to operate over the extended temperature range.

[READ MORE](#)

New Software Update for the TimeProvider 5000 PTP Grandmaster Clock

Microchip is proud to announce the latest software update to the [TimeProvider 5000 PTP](#) Grandmaster system. Recent release 4.0.5 provided major feature enhancements such as doubling system capacity to 2000 PTP clients, and IPv6 support. New maintenance release 4.0.7 builds on feature group 4 providing two SW fixes to enhance system performance. For more information, please log into our support site and search on “TimeProvider 5000” for a complete list of available files and SW downloads.

[Support Site](#)



Microchip releases 53100A phase noise analyzer for measurement of atomic clocks and high-performance crystal oscillators

Measuring RF signals up to 200MHz, the 53100A characterizes phase noise, jitter, Allan deviation and time deviation. As a high-performance analyzer, the [53100A](#) is small enough to be integrated into manufacturing ATE, yet powerful enough for precision oscillator measurements. [Product Information](#)

Watch the Phase Noise Analyzer Update!



DISA Certifies Microchip Time and Frequency References!

DISA JITC certifies the [SyncServer S600/S650](#), [TimeProvider 4100](#) & [5071A Cesium](#) for Cybersecurity/Interoperability compliance and are certified for joint military use. These models have been added to the DISA DoDIN Approved Product List (APL) and easier than ever for US DoD customers to purchase.

[Learn More](#)



[TimeProvider® 4100](#) Newest Release is Available Now

[Version 2.1](#) main new feature is Multi-Domain High Performance Boundary Clock (MD HP BC) with Class D (5ns) Time Error performance.

MD HP BC is a critical capability to enable Virtual PRTC solution (vPRTC) to be deployed. vPRTC enables operators to become less dependent on GPS/GNSS and to protect phase over large networks.

[Key Resource](#) and [News Release](#)

Watch the TimeProvider 4100 News Update!



SYNC Up Corner

~ Blog: Enabling precise time end-to-end from ePRTC to NodeBs.

Because timing is becoming more precise and requires not only the “easy” frequency use case but also the more difficult to achieve “phase,” you need enhanced technology in your network... Eric Colard

[Read the entire Blog here](#)

~ Article: 5G Changes Network Timing Architectures

Network timing has always been a critical component of wireless services. With its higher data rates and low latency, 5G will need more reliable timing than did older technologies... [Read the featured article by Jim Olsen today!](#)

~ GPS World Article: Distributing high precise time over optical networks in the 5G world.

Mobile operators are investing heavily in the deployment of LTE-Advanced and 5G networks. Their success will depend on how well they address today's critical GPS/GNSS vulnerabilities. [Informative article](#) by Eric Colard



Microchip Sponsored Virtual 2020 WSTS Conference

We have been proud sponsors of the Workshop on Synchronization and Timing Systems (WSTS) for over 20 years. In May Microchip's Joe Neil provided an innovative presentation on “Precision Timing in Smart Cities”, among five other industry topics. The slides and conference replay are available at: <https://wstsconference.com/>

